



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Peter K. Craft, et al.

Ser. No.: 09/970,124

Filing Date: October 2, 2001

Examiner: Larry D. Donaghue

Atty. Docket No.: ALA-020

GAU: 2154

For: NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES  
SOLICITED SESSION LAYER READ COMMANDS

November 16, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

9<sup>th</sup> Supplemental Information Disclosure Statement per 37 C.F.R. §1.98

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicants bring sixty-one documents to the Examiner's attention. Included are copies of twenty non-patent reference documents, four foreign patent reference documents, and a four-page form PTO-1449 listing these documents separately from thirty-six U.S. Patent reference documents. Copies of the thirty-six U.S. Patent reference documents are not enclosed.

Citation of these documents shall not be construed as an admission that the documents are prior art with respect to the instant invention, a representation that a search has been made, or an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

Respectfully submitted,

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CERTIFICATE OF MAILING

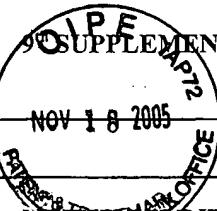
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on November 16, 2005.

Date: 11-16-05

  
Mark Lauer

U.S. Department of Commerce, Patent and Trademark Office

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Filing date: October 2, 2001

Inventors: Peter K. Craft, et al.

Group Art Unit: 2154

Examiner name: Larry D. Donaghue

Attorney Docket No. ALA-020

**U.S. PATENT DOCUMENTS**

| *Examiner Initial |  | Document Number | Date     | Name                  | Class | Subclass | Filing Date, If Appropriate |
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| 1                 |  | 4,589,063       | 5/13/86  | Shah et al.           | 710   | 8        |                             |
| 2                 |  | 5,642,482       | 6/24/97  | Pardillos             | 395   | 200.2    |                             |
| 3                 |  | 5,701,516       | 12/23/97 | Cheng et al.          | 395   | 842      |                             |
| 4                 |  | 5,778,013       | 7/7/98   | Jedwab                | 714   | 807      |                             |
| 5                 |  | 5,809,328       | 09/15/98 | Nogales et al.        | 395   | 825      |                             |
| 6                 |  | 5,931,918       | 08/03/99 | Row et al.            | 709   | 300      |                             |
| 7                 |  | 5,935,205       | 08/10/99 | Murayama et al.       | 709   | 216      |                             |
| 8                 |  | 5,937,169       | 08/10/99 | Connery et al.        | 395   | 200.8    |                             |
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| 22                |  | 6,141,705       | 10/31/00 | Anand et al.          | 710   | 15       |                             |
| 23                |  | 6,145,017       | 11/7/00  | Ghaffari              | 710   | 5        |                             |
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| 25                |  | 6,172,980       | 1/9/01   | Flanders et al.       | 370   | 401      |                             |
| 26                |  | 6,202,105       | 3/13/01  | Gates et al.          | 710   | 20       |                             |
| 27                |  | 6,226,680       | 05/01/01 | Boucher et al.        | 709   | 230      |                             |

Examiner Date Considered

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

|  |  |                                   |
|--|--|-----------------------------------|
| U.S. Department of Commerce, Patent and Trademark Office                                       |  | Application No.: 09/970,124       |
| <b>9<sup>th</sup> SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>               |  | Filing date: October 2, 2001      |
|  |  | Inventors: Peter K. Craft, et al. |
|  |  | Group Art Unit: 2154              |
| <b>NETWORK INTERFACE DEVICE THAT FAST-PATH PROCESSES SOLICITED SESSION LAYER READ COMMANDS</b> |  | Examiner name: Larry D. Donaghue  |
|  |  | Attorney Docket No. ALA-020       |

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| *Examiner Initial |    | Document Number | Date     | Name           | Class | Subclass | Filing Date, If Appropriate |
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|                   | 29 | 6,247,060       | 06/12/01 | Boucher et al. | 709   | 238      |                             |
|                   | 30 | 6,279,051       | 8/21/01  | Gates et al.   | 710   | 20       |                             |
|                   | 31 | 6,298,403       | 10/2/01  | Suri et al.    | 710   | 100      |                             |
|                   | 32 | 2001/0053148    | 12/20/01 | Bilic et al.   | 370   | 389      |                             |
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|  | 37 | WO 00/13091     | 3/09/00  | PCT/US98/24943 |       |             |     |    |
|  | 38 | WO 01/40960     | 6/07/01  | PCT/US00/32660 |       |             |     |    |
|  | 39 | WO 01/59966     | 8/16/01  | PCT/US00/06475 |       |             |     |    |
|  | 40 | WO 01/86430     | 11/15/01 | PCT/US01/15180 |       |             |     |    |

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|                   | 41       | Internet pages of Xpoint Technologies, Inc. entitled "Smart LAN Work Requests", 5 pages, printed 12/19/97. |
|                   | 42       | Richard Stevens, "TCP/IP Illustrated, Volume 1, The Protocols", pages 325-326 (1994).                      |
|                   | 43       | Internet pages entitled: Northridge/Southbridge vs. Intel Hub Architecture, 4 pages, printed 2/19/01.      |

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| <b>OTHER ART—NON PATENT LITERATURE DOCUMENTS</b>   |          |  |
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|  | 44       | Gigabit Ethernet Technical Brief, Achieving End-to-End Performance. Alteon Networks, Inc., First Edition, September 1996, 15 pages.  |
|  | 45       | Internet pages directed to Technical Brief on Alteon Ethernet Gigabit NIC technology, www.alteon.com, 14 pages, printed 3/15/97.   |
|  | 46       | VIA Technologies, Inc. article entitled "VT8501 Apollo MVP4", pages i-iv, 1-11, cover and copyright page, revision 1.3, Feb. 1, 2000.  |
|  | 47       | "Two-Way TCP Traffic Over Rate Controlled Channels: Effects and Analysis", by Kalampoukas et al., IEEE Transactions on Networking, vol. 6, no. 6, December 1998, 17 pages.                                 |
|  | 48       | iReady News article entitled "Toshiba Delivers First Chips to Make Consumer Devices Internet-Ready Based on iReady Design", Santa Clara, CA, and Tokyo, Japan, October 14, 1998, printed 11/2/98, 3 pages. |
|  | 49       | "File System Design For An NFS File Server Appliance", Article by D. Hitz, et al., 13 pages.   |
|  | 50       | Adaptec Press Release article entitled "Adaptec Announces EtherStorage Technology", 2 pages, May 4, 2000, printed 6/14/00.   |
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|  | 53       | CIBC World Markets article entitled "Computers; Storage", by J. Berlino et al., 9 pages, dated August 7, 2000.   |
|  | 54       | Merrill Lynch article entitled "Storage Futures", by S. Milunovich, 22 pages, dated May 10, 2000.  |
|  | 55       | CBS Market Watch article entitled "Montreal Start-Up Battles Data Storage Bottleneck", by S. Taylor, dated March 5, 2000, 2 pages, printed 3/7/00.   |
|  | 56       | Internet-draft article entitled "SCSI/TCP (SCSI over TCP)", by J. Satran et al., 38 pages, dated February 2000, printed 5/19/00.   |
|  | 57       | WindRiver article entitled "Tornado: For Intelligent Network Acceleration", copyright Wind River Systems, 2001, 2 pages.   |
|  | 58       | WindRiver White Paper entitled "Complete TCP/IP Offload for High-Speed Ethernet Networks", Copyright Wind River Systems, 2002, 7 pages.  |
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|  | 60       | Thla, Y.H. Publication entitled "High-Speed OSI Protocol Bypass Algorithm with Window Flow Control", <u>Protocols for High Speed Networks</u> , pages 53-68, 1993.                        |
|  | 61       | Thla, Y.H. Publication entitled " A Reduced Operational Protocol Engine (ROPE) for a multiple-layer bypass architecture", <u>Protocols for High Speed Networks</u> , pages 224-239, 1995. |
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